



**ANJUMAN-I-ISLAM'S  
KALSEKAR TECHNICAL CAMPUS, NEW PANVEL**

Approved by : All India Council for Technical Education, Council of Architecture, Pharmacy Council of India New Delhi,  
Recognised by : Directorate of Technical Education, Govt. of Maharashtra, Affiliated to : University of Mumbai.

- SCHOOL OF ENGINEERING & TECHNOLOGY
- SCHOOL OF PHARMACY
- SCHOOL OF ARCHITECTURE

**DEPARTMENT OF ELECTRICAL ENGINEERING**

REV: 00

**EXPERT LECTURE REPORT**

IIC-03

Date: 18<sup>th</sup> October, 2024

School/Department: SoET Electrical & Computer Engineering Department

Name of resource person:	Prof. Shreya Sawant
Designation:	Asst. Professor Samarth Academy Kankavli Sindhudurg
Contact details:	8433587150 Email-Id:sawantshreya333@gmail.com
Date of expert lecture:	06 <sup>th</sup> October, 2024
Title of Lecture:	Electrical Circuit Analysis
Organized by :	EE AIKTC, New Panvel, Navi Mumbai.
Target audience:	Second Year Electrical Students

**DETAILS OF EXPERT LECTURE:**

Aims/Objectives:	The objective of expert session is to impart knowledge about Electrical Circuit Analysis Course
Description of Expert lecture:	An expert talk on “Electrical Circuit Analysis” was organized by Anjuman-I-Islam’s Kalsekar Technical Campus by SoET Electrical & Computer Engineering Department. Asst. Prof. Shradha A. Sawant introduced resource persons of the event to the participants. The resource person handled the session on electrical circuit analysis. The session covers the topics on Laplas Transformation, Pole Zero network and network Function. Totally, there are all second-year students of ECE department attended the session via Zoom Online Platform.

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*Collaboration of IEI Students' Chapter and Department of Electrical & Computer Engineering Organize Expert Talk on*

**Electrical Circuit Analysis**

SUN  
DAY

OCT  
06

TIME  
9:30  
AM

Speakers Name

**Prof. Shreya Sawant**

Innovative Teaching - Exuberant Learning

Vision : To be the most sought after academic, research and practice based department of Electrical Engineering that others would wish to emulate.

hw

$$Z(s) = \frac{(s+2)}{(s+1+4j)(s+1-4j)}$$

$$Z(s) = \frac{H(s+2)}{(s+1)^2 + (4)^2} = \frac{H(s+2)}{s^2 + 2s + 1 + 16} = \frac{H(s+2)}{s^2 + 2s + 17}$$


Saim Zahoor Fame

surajsahu23ee14

21EE08 Irfan shaikh

## Electrical Circuit Analysis

### Content

- Network function-Poles and Zeros

- How to solve numerical based on ladder network
- How to plot pole zero diagram
- Restriction on pole zeros location for driving point function and transfer function
- Time domain behaviour of pole zero plot.

- Electrical circuit analysis using Laplace Transform



### Outcome of Expert Lecture:

COs addressed: Students are able

- Analyse time domain behaviour from pole zero plot.
- Develop and analyse transfer function model of system using two port network parameters

### PSOs addressed:

CO	P	PO	PO	PO	PO	PO	PO	PO	PO	PO	PO	PO
	O1	2	3	4	5	6	7	8	9	10	11	12
CO1	2	1									1	1
CO2	2	1									1	1

### Pos addressed:

CO	PSO1	PSO2
CO1	1	1
CO2	1	1

Course Owner

(Asst. Prof. Shraddha Sawant)

HOD

(Dr. Afzal Shaikh)

Dean of SoET

(Dr. Rajendra. B. Magar)

Director

(Dr. Ramjan A. Khatik)